

Constructive Polychronous Systems

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Synchronous Languages

- Esterel, Lustre, Signal, ...
 - A. Benveniste, P. Caspi, S. Edwards, N. Halbwachs, P. Le Guernic and R. de Simone. *The Synchronous Languages Twelve Years Later*. Proceedings of the IEEE, 2003.
- domain-specific languages for embedded system design
- discrete control programs for continuous, physical environments
- express concurrency in a user-friendly manner
- same mathematical foundation: “the synchronous hypothesis”, ...

..., what ?

- same mathematical foundation: “the synchronous hypothesis”, but:
 - different visuals: block diagrams, hierarchical automata, ...
 - different styles: imperative, dataflow, ...
 - different models: reactive, synchronous, polychronous, ...
 - different properties: constructive, determinism, endochrony, ...

Goal

Use the Signal/Polychrony toolset in the Averest/Quartz environment

Why ? ...

- ... use the Polychrony toolset in the Averest environment
 - Gain productivity from communication and computation scheduling from automatic synthesis
 - Write sound functional modules
 - Automate scheduler synthesis
 - Implied programming methodology
 - Specify/program imperative reactive modules in Quartz
 - Synthesize their scheduler from a Signal network specified by constraints, interfaces, contracts

Needs

A constructive framework encompassing reactive Quartz modules and endochronous Signal networks

How ?

A common constructivity framework

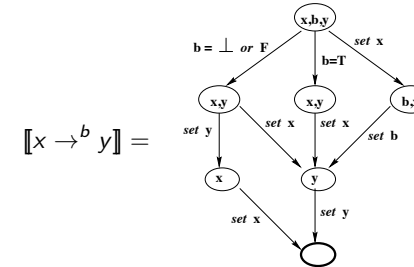
- Stabilization of electric circuits [Huffman, '71, Malik, '94]
- Electrical semantics of Esterel [Shiple, '96, Berry, '99]
- Constructive Boolean circuits [Mendler, Shiple, Berry, '12]

Goal

- A lattice and fixpoint theory for clocked streams
- A common semantic framework for Quartz and Signal
- An executable structured operational semantics of Signal

Starting point

- Game-theoretical micro-automata for Signal [Benveniste et al., 2000]

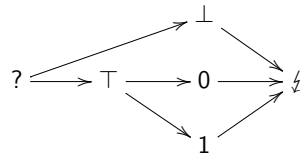


First sub-goal

⇒ Define a complete lattice / fixpoint theory for multi-clocked systems

A lattice for the status of variables/signals

- ? signal status is unknown
- ⊥ signal is absent or inhibited
- ⊤ signal is present or activated
- ⚡ signal is inconsistent



Basic principle

- A complete lattice of signal and variable status
- Monotonic functions to define status transitions
- A constructive fixed-point theory

Value of a boolean operation

Start from the truth table

\wedge^D	?	⊥	⊤	0	1	⚡
?						
⊥						
⊤						
0				0	0	
1				0	1	
⚡						

Status of a boolean operation

Fill it with all possible errors

$\wedge^{\mathcal{D}}$?	\perp	T	0	1	\downarrow
?						\downarrow
\perp			\downarrow	\downarrow	\downarrow	\downarrow
T		\downarrow				\downarrow
0		\downarrow		0	0	\downarrow
1		\downarrow		0	1	\downarrow
\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow

Quartz as synchronous guarded actions

Model Quartz programs as guarded actions on (continuous) variables

- $p, q ::= \gamma \Rightarrow x = \tau$ (immediate assignment)
- | $\gamma \Rightarrow \text{next}(x) = \tau$ (delayed assignment)
- | $\text{init}(x) = \tau$ (initialization)

- | $p | q$ (composition)
- | $\text{var } x: p \text{ default } v$ (action block)

Status of a boolean operation

Complete by being positive (monotonic)

$\wedge^{\mathcal{D}}$?	\perp	T	0	1	\downarrow
?	?	\perp	T	0	T	\downarrow
\perp	\perp	\perp	\downarrow	\downarrow	\downarrow	\downarrow
T	T	\downarrow	T	0	T	\downarrow
0	0	\downarrow	0	0	0	\downarrow
1	T	\downarrow	T	0	1	\downarrow
\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow

Small-step semantics $s, p \rightarrow s', q$ of Quartz (actions)

For $\star \in \{\text{and, or}\}$, define $s, x \star y \rightarrow v$ iff $v = s(x) \star s(y) \in \mathbb{B}$

$$\frac{s, \gamma \rightarrow 0}{s, \gamma \Rightarrow x = \tau \rightarrow s, \text{done}(\gamma \Rightarrow x = \tau)}$$

$$\frac{s, \gamma \rightarrow 1 \quad s, \tau \rightarrow v}{s, \gamma \Rightarrow x = \tau \rightarrow s \uplus (x, v), \text{done}(\gamma \Rightarrow x = \tau)}$$

Small-step semantics of Quartz (delayed actions)

$$s, \text{init}(x) = v \rightarrow s \uplus (x, v), ()$$

$$\frac{s, \gamma \rightarrow 0}{s, \gamma \Rightarrow \text{next}(x) = \tau \rightarrow s, \text{done}(\gamma \Rightarrow \text{next}(x) = \tau)}$$

$$\frac{s, \gamma \rightarrow 1 \quad s, \tau \rightarrow v}{s, \gamma \Rightarrow \text{next}(x) = \tau \rightarrow s, \text{done}(\text{init}(x) = v \mid \gamma \Rightarrow \text{next}(x) = \tau)}$$

Small-step semantics of Quartz (scheduling)

$$\frac{s' = (s(x) \in \mathcal{D})?s, s \uplus (x, v) \quad w = (x \in \mathcal{M})?s'(x), v}{s, \text{var } x: \text{done}(p) \text{ default } v \rightarrow s', \text{done}(\text{var } x: p \text{ default } w)}$$

$$\frac{s, p \rightarrow s', p'}{s, p \mid q \rightarrow s', p' \mid q} \quad \frac{s, p \rightarrow s', p'}{s, q \mid p \rightarrow s', q \mid p'}$$

$$s, \text{done } p \mid \text{done } q \rightarrow s, \text{done}(p \mid q)$$

Semantics $s, p \rightarrow s', q$ for equations over signals (function)

Model a polychronous network as equations over clocked signals

$$\frac{s(x, y, z) \rightarrow_{\text{and}} (a, b, c)}{s, x := y \text{ and } z \rightarrow s \uplus (x, a)(y, b)(z, c), x := y \text{ and } z}$$

Propagate knowledge of absence

$s(x)$	$s(y)$	$s(z)$	a	b	c
\perp	$?/\perp$	$?/\perp$	\perp	\perp	\perp
$?/\perp$	\perp	$?/\perp$	\perp	\perp	\perp
$?/\perp$	$?/\perp$	\perp	\perp	\perp	\perp

Equations over signals (function)

$$\frac{s(x, y, z) \rightarrow_{\text{and}} (a, b, c)}{s, x := y \text{ and } z \rightarrow s \uplus (x, a)(y, b), (z, c), x := y \text{ and } z}$$

Propagate knowledge of presence

$s(x)$	$s(y)$	$s(z)$	a	b	c
\top	$?/\top$	$?/\top$	\top	\top	\top
$?/\top$	\top	$?/\top$	\top	\top	\top
$?/\top$	$?/\top$	\top	\top	\top	\top

Equations over signals (function)

$$\frac{s(x, y, z) \rightarrow_{\text{and}} (a, b, c)}{s, x := y \text{ and } z \rightarrow s \uplus (x, a)(y, b), (z, c), x := y \text{ and } z}$$

Progress by computations

s(x)	s(y)	s(z)	a	b	c
?/T	a	?/T	T	a	T
?/T	?/T	a	T	T	a
?/T	b	a	a ∧ b	b	a

Notice that \rightarrow_{and} is monotonic and increasing

* $a, b \in \mathbb{B}$

Equations over signals (merging)

$$s(x, y, z) \rightarrow_{\text{default}} (a, b, c)$$

Progress by the logic of absence, presence and by computations

s(x)	s(y)	s(z)	a	b	c
⊥	?/⊥	?/⊥	⊥	⊥	⊥
?/⊥	⊥	⊥	⊥	⊥	⊥
?	T	x*	T	T	x
?	x	T	T	x	T
?/T	a	x	a	a	x
?/T	⊥	a	a	⊥	a

* x is the "don't care", i.e., any status except error

Equations over signals (sampling)

$$s(x, y, z) \rightarrow_{\text{when}} (a, b, c)$$

Progress by the logic of absence, presence and by computations

s(x)	s(y)	s(z)	a	b	c
⊥	?/⊥	?/⊥	⊥	⊥	⊥
?/⊥	⊥	?/⊥	⊥	⊥	⊥
?/⊥	?/⊥	⊥	⊥	⊥	⊥
?/⊥	x	0	⊥	x	0
T	?/T	b	T	T	a ∨ 1
?/T	a	1	a	a	1

* Rules for absence/presence encode the clock calculus

Equations over signals (delay)

$$\frac{s(x, y), a \rightarrow_{\text{init}} (b, c, d)}{s, x := y \text{ init } a \rightarrow s \uplus (x, b)(y, c), x := y \text{ init } d}$$

Progress by the logic of absence, presence and by computations

s(x)	s(y)	a	b	c	d
⊥	?/⊥	a	⊥	⊥	a
?/⊥	⊥	a	⊥	⊥	a
T	?/T	a	a	T	a
?/T	T	a	a	T	a
?/⊥	b	a	a	b	b
a	b	a	a	b	b

... Wait!

Equations over signals (delay)

... Where you listening ?

$$\frac{s(x, y), a \rightarrow_{\text{init}} (b, c, d)}{s, x := y \text{ sinit } a \rightarrow s \uplus (x, b)(y, c), x := y \text{ sinit } d}$$

It's not quite

$s(x)$	$s(y)$	a	b	c	d
$?/\perp$	b	$a \not\sqsubseteq$	a	b	b
a	b	$a \not\sqsubseteq$	a	b	b

but more exactly

$s(x)$	$s(y)$	a_{t-1}	a_t	b	c	a_{t-1}	d
$?/\perp$	b	a	$?/\top$	\sqsubseteq	a	b	a
a	b	a	$?/\top$	\sqsubseteq	a	b	a

Equations over signals (a run)

$$c := o \text{ sinit } 1 \mid o := n \text{ default } \overbrace{c-1}^x \mid n \hat{=} \text{ when } \overbrace{c=0}^y$$

$$\begin{aligned} (c, ?)(n, ?)(o, \top)(x, ?)(y, ?) &\rightarrow_{\text{init}} (c, 1)(n, ?)(o, \top)(x, ?)(y, ?) \\ &\rightarrow_{\text{sub}} (c, 1)(n, ?)(o, \top)(x, 0)(y, ?) \\ &\rightarrow_{\text{eq}} (c, 1)(n, ?)(o, \top)(x, 0)(y, \text{ff}) \\ &\rightarrow_{\hat{=}} (c, 1)(n, \perp)(o, \top)(x, 0)(y, \text{ff}) \\ &\rightarrow_{\text{default}} (c, 1)(n, \perp)(o, 0)(x, 0)(y, \text{ff}) \\ &\rightarrow_{\text{init}} (c, 1)(n, \perp)(o, 0)(x, 0)(y, \text{ff}) \end{aligned}$$

$$c := o \text{ sinit } 0 \mid \dots$$

From synchrony to asynchrony

Model FIFO buffers ...

$$w ::= \begin{array}{l} \epsilon \quad (\text{empty}) \\ | a.w \quad (\text{read}) \\ | w.a \quad (\text{write}) \end{array}$$

... to define the interface of a process p with the network P

$$P, Q ::= \langle s, p \rangle \mid P \parallel Q \quad (\text{network})$$

... and get something like that: progress with \rightarrow and writeout with \rightarrow^*

$$E, \langle s_0, p \rangle \rightarrow^* F, \langle s, q \rangle \rightarrow^* G, \langle s_0, r \rangle \quad (\text{big step})$$

$$s_0 = \{(x, ?) \mid x \in V(p)\}$$

Interface semantics $E, P \rightarrow F, Q$ (trigger and read)

$$\frac{s, p \rightarrow t, q}{E, \langle s, p \rangle \rightarrow E, \langle t, q \rangle} \quad (\text{embedding rule})$$

$$\frac{x \in T(p)}{E, \langle s \uplus (x, ?), p \rangle \rightarrow E, \langle s \uplus (x, \top), p \rangle} \quad (\text{trigger execution})$$

$$\frac{x \in I(p)}{E \uplus (x, a.w), \langle s \uplus (x, \top), p \rangle \rightarrow E \uplus (x, w), \langle s \uplus (x, a), p \rangle} \quad (\text{read inputs})$$

Interface semantics $E, P \rightarrow F, Q$ (write and sync)

$$\frac{x \in O(p) \wedge a \in \mathbb{B}}{E \uplus (x, w), \langle s \uplus (x, a), p \rangle \rightarrow E \uplus (x, w.a), \langle s, p \rangle} \quad (\text{write output})$$

$$\frac{x \notin O(p) \vee a = \perp}{E, \langle s \uplus (x, a), p \rangle \rightarrow E, \langle s \uplus (x, ?), p \rangle} \quad (\text{filter locals, absence})$$

Interface semantics $E, P \rightarrow F, Q$ (reactive modules)

Interface semantics of Quartz is much simpler:

$$(\text{read}) \quad E \uplus (x, a.w), \langle s, p \rangle \rightarrow E \uplus (x, w), \langle s_x \uplus (x, a), p \rangle \quad x \in I(p)$$

$$(\text{write}) \quad E \uplus (x, w), \langle s \uplus (x, a), p \rangle \rightarrow E \uplus (x, w.a), \langle s_x \uplus (x, ?), p \rangle \quad x \in O(p)$$

$$(\text{mask}) \quad E, \langle s, p \rangle \rightarrow E, \langle s_x \uplus (x, ?), p \rangle \quad x \in L(p)$$

$$(\text{restart}) \quad E, \langle s, \text{done } p \rangle \rightarrow E, \langle s, p \rangle$$

Equations over clocked signals (interface with streams)

$$c := o \text{ sinit } 0 \mid o := n \text{ default } \overbrace{c-1}^x \mid n \hat{=} \text{ when } \overbrace{c=0}^y$$

$$\begin{array}{l} \vdots \\ \rightarrow_{\$init} [(n, 42)(o, \epsilon)] \vdash (c, ?) (n, ?) (o, \top) (x, ?) (y, ?) \\ \rightarrow_{sub} [(n, 42)(o, \epsilon)] \vdash (c, 0) (n, ?) (o, \top) (x, ?) (y, ?) \\ \rightarrow_{eq} [(n, 42)(o, \epsilon)] \vdash (c, 0) (n, ?) (o, \top) (x, -1) (y, tt) \\ \rightarrow_{\hat{=}} [(n, 42)(o, \epsilon)] \vdash (c, 0) (n, \top) (o, \top) (x, -1) (y, tt) \\ \rightarrow [(n, \epsilon_{\bullet})(o, \epsilon)] \vdash (c, 0) (n, 42_{\bullet})(o, \top) (x, -1) (y, tt) \quad (\text{read}) \\ \rightarrow_{default} [(n, \epsilon)(o, \epsilon)] \vdash (c, 0) (n, 42) (o, 42) (x, -1) (y, tt) \\ \rightarrow_{\$init} [(n, \epsilon)(o, \epsilon)] \vdash (c, 0) (n, 42) (o, 42) (x, -1) (y, tt) \\ \rightarrow^* [(n, \epsilon)(o, 42)_{\bullet}] \vdash () \quad (\text{write}) \end{array}$$

$$c := o \text{ sinit } 42 \mid \dots$$

A constructive GALS semantics

$$\frac{E, P \rightarrow F, Q}{E, P \rightarrow E, Q} \quad (\text{step})$$

$$\frac{E, \langle s, p \rangle \rightarrow^* F, \langle s', q \rangle}{E, \langle s, p \rangle \rightarrow F, \langle s', q \rangle} \quad (\text{flush})$$

$$\frac{E, P \rightarrow E', P'}{E, P \parallel Q \rightarrow E', P' \parallel Q} \quad \frac{E, P \rightarrow E', P'}{E, P \parallel Q \rightarrow E', P' \parallel Q} \quad (\text{schedule})$$

Formal properties

Definition (Synchronous constructivity)

A module p is synchronous constructive iff.

for all $s_0 = \{(x, a) \mid x \in I(p), a \in \mathbb{B}\} \cup \{(y, ?) \mid y \in V(p) \setminus I(p)\}$
we have $s_0, p \rightarrow^* s, q$ and s is defined on \mathbb{B} (i.e. $s = \text{Ifp} \rightarrow_p(s_0)$)

Proposition

If p is synchronously constructive then p is reactive*

* a.k.a. synchronously deterministic

Formal properties

Definition (Asynchronous constructivity)

A process p is asynchronously constructive iff.

for any environment E of non-empty streams defined on $V(p)$
and $s_0 = \{(x, ?) \mid x \in V(p)\}$
we have $E, \langle s_0, p \rangle \rightarrow^* F, \langle s, q \rangle$
and s is defined on \mathbb{B}^\perp (i.e. $(F, s) = \text{Ifp} \rightarrow_p(E, s_0)$).

Proposition

If p is asynchronously constructive then p is asynchronously deterministic

No reaction to absence

A process should stutter when its triggers are inhibited

$$\frac{x \in T(p)}{E, \langle s \uplus (x, ?), p \rangle \rightarrow E, \langle s \uplus (x, \perp), p \rangle}$$

Definition (Stuttering)

A process p is robust to stuttering iff.

for $s = \{(x, \perp) \mid x \in T(p)\} \cup \{(x, ?) \mid x \in V(p) \setminus V(p)\}$
we have $\langle s, p \rangle \rightarrow^* \langle s', p \rangle$ and $s' = \{(x, \perp) \mid x \in V(p)\}$

Note: this captures (weakly) endochronous systems

Discussions and future works

- An executable operational semantics of Signal and Quartz
 - simulates scheduling of data-flow equations
 - takes into account their interface with streams
- A common constructive model for Quartz and Signal

- ⇒ Extend constructivity analysis to polychronous (concurrent) systems
- ⇒ A constructivity framework for abstract interpretation

Oversampling and clock domains

Define the sum $\sum_{i=1}^n$ from the counter (triggered by o),

$$\mathit{sigma}(n, s) \triangleq (\mathit{counter}(n, o) \mid (i := (\mathit{isinit}0) + o \mid s := i \text{ when } (o = 1)) / i) / o$$

Sigma is triggered by o , and, equivalently

$$\mathit{sigma}'(n, s) \triangleq (\mathit{counter}(n, o) \parallel (i := (\mathit{isinit}0) + o \mid s := i \text{ when } (o = 1)) / i) / o$$